

Notice of References Cited	Application/Control No. 10/603,439		Applicant(s)/Patent Under Reexamination SHAYLOR ET AL.	
	Examiner J. Derek Rutten		Art Unit 2192	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,815,718 A	09-1998	Tock, Theron D.	717/166
*	B	US-5,999,732 A	12-1999	Bak et al.	717/148
*	C	US-6,223,346 B1	04-2001	Tock, Theron D.	717/166
*	D	US-6,253,215 B1	06-2001	Agesen et al.	707/206
*	E	US-6,339,841 B1	01-2002	Merrick et al.	717/166
*	F	US-6,584,612 B1	06-2003	Mueller et al.	717/166
*	G	US-6,779,732 B2	08-2004	Krishna et al.	235/492
*	H	US-6,883,163 B1	04-2005	Schwabe, Judith E.	717/126
*	I	US-6,970,948 B2	11-2005	Brown et al.	710/8
*	J	US-6,983,460 B1	01-2006	Goire et al.	717/175
*	K	US-7,131,121 B2	10-2006	Krishna et al.	717/165
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shaylor, N., Simon, D. N., and Bush, W. R. 2003. A java virtual machine architecture for very small devices. In Proceedings of the 2003 ACM SIGPLAN Conference on Language, Compiler, and Tool For Embedded Systems (San Diego, California, USA, June 11 - 13, 2003). LCTES '03. ACM Press, New York, NY, 34-41.
	V	Shaylor, N. 2002. A Just-in-Time Compiler for Memory-Constrained Low-Power Devices. In Proceedings of the 2nd Java™ Virtual Machine Research and Technology Symposium (August 01 - 02, 2002). S. P. Midkiff, Ed. USENIX Association, Berkeley, CA, 119-126
	W	Zhiqun Chen and Rinaldo Di Giorgio, Understanding Java Card 2.0, JavaWorld.com, 03/01/98, accessed and printed 11/17/2006 from <http://www.javaworld.com/javaworld/jw-03-1998/jw-03-javadev.html>, 12 pages
	X	Goldberg, A. 1998. A specification of Java loading and bytecode verification. In Proceedings of the 5th ACM Conference on Computer and Communications Security (San Francisco, California, United States, November 02 - 05, 1998). CCS '98. ACM Press, New York, NY, 49-58

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/603,439	Applicant(s)/Patent Under Reexamination SHAYLOR ET AL.	
	Examiner J. Derek Rutten	Art Unit 2192	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chen, Java Card™ Technology for Smart Cards, Addison Wesley Professional, June 06, 2000, Chapter 3, 20 Pages
	V	Leroy, "On-Card Bytecode Verification for Java Card", 2001, Springer Berlin / Heidelberg, Lecture Notes in Computer Science, V. 2140, pp. 150-164
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.